

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re U.S. Patent Application of )  
MIZUNO )  
Application Number: To be assigned )  
Filed: Concurrently Herewith )  
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE )  
ATTORNEY DOCKET NO. HITA.0419 )

Honorable Assistant Commissioner  
for Patents  
Washington, D.C. 20231

**PRELIMINARY AMENDMENT**

Sir:

Applicant has amended the claims in accordance with standard U.S. practice. Prior to an examination on the merits, please amend the above-identified application as follows:

**IN THE SPECIFICATION:**

Please insert before the first paragraph under Background of the Invention of page 1 of the Disclosure currently on file with the following paragraph:

This application is a continuation application of U.S. Application No, 10/350,084 filed on January 24, 2003, which is a continuation application of U.S. Application No. 09/874,017 filed on June 6, 2001.

**IN THE CLAIMS:**

Please delete claims 1-16.

Please add the following claims.

17. A semiconductor integrated circuit device comprising:
- a power control circuit;
  - a plurality of first circuits, each of the plurality of first circuits having at least an active state and a standby of state; and
  - a second circuit to be privileged to access each of the first circuits and issue a request to the power control circuit in order to access one of the first circuits;